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Verification of Autonomous Systems

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I. OVERVIEW

The robotics and autonomous systems communities have seen a significant and rapid increase in both the development of robots and vehicles for commercial use and in using these systems across a wide range of novel applications. As these robots, vehicles, software and even embedded devices move towards much greater autonomy, techniques for verification, providing much higher confidence than usual, are becoming required. However, the analysis and evaluation processes used for traditional systems must be significantly enhanced to provide increased confidence in this next wave of *autonomous* systems. The need for well understood and effective verification techniques will become even vital, as we move to commercial applications that rely on complex Artificial Intelligence (AI) technologies, and the utilisation of these systems in safety-critical scenarios.

There are a growing number of research developments concerning the verification of complex systems that can all impact upon this problem. These are clearly of relevance for designing, constructing, and deploying autonomous systems, but also have importance to Psychology (e.g., social robotics), Philosophy (e.g., machine ethics), and Law (e.g., certification). Furthermore, constructing autonomous systems without strong behavioral guarantees can lead to serious outcomes and may consequently hold back the widespread adoption of these systems.

As the research is currently fragmented and often not well publicized, this Technical Committee* aims to coalesce this activity, drive the research agenda forward, and instill the necessity for verification firmly within industry, government, standards and the public.

II. BACKGROUND

Following a number of workshops and collaborations, the *Verification of Autonomous Systems* Technical Committee (TC-VAS) was established within the IEEE Robotics and Automation Society in 2019. It has now developed into a large community, with a mailing list of over 490 unique individuals¹, monthly webinars, workshops and input to standards activities.

* www.ieee-ras.org/verification-of-autonomous-systems

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¹493 unique people; 529 emails — checked on 2021/10/23

III. WHAT IS 'VERIFICATION'?

The term *verification* encompasses a range of techniques meant to assess, with varying degrees of strength, whether a particular system matches its required behaviour. Essentially, it consists in providing evidence that some system, **S**, matches its requirements, **R**. Since the borders between verification and validation can be blurred, this Technical Committee takes an inclusive approach to such classifications. The problem of defining requirements that correctly and completely describe the actual desired behavior is likewise included.

Verification techniques are often categorised into those that have a basis in mathematical logic/proof and those that rely on more empirical approaches. Within each of these classes, there remain many options. For example, within the former (termed *formal* verification), one might employ [8]:

- **Proof** — where a formal proof is carried out to establish that **R**, encoded as a logical formula, follows from the logical description of the behaviour of **S**;
- **Model Checking** — where **R** is exhaustively assessed against a representation of all possible execution paths of **S**;
- **Runtime Verification** — where **R** is assessed against the system **S** *as it is executing* [10].

All these above-mentioned options also have probabilistic versions, many of which have become popular in recent years [2], [7].

There are also a range of *empirical* verification approaches, such as:

- **Software Testing** — where **R** is checked on a subset of the possible executions of **S**;
- **Simulation-based Testing** — as above but where a *simulation* of the real environment is used for environmental modelling and interactions [1];
- **In-situ Testing** — where **R** is assessed against the actual working of the system in a real-world environment [3].

Again, there are many variations and options here ranging from the 'certain' (in the case of formal proof) to the stochastic. The Technical Committee aims to encompass work across all these areas as well as to link and support aspects such as transparency [11] and modularity [9], [6].

IV. WHAT IS 'AUTONOMY'?

Essentially, **autonomy** is the ability (and often requirement) of a system to make its own decisions and take its own actions. This Technical Committee takes an inclusive view on system autonomy, covering *full autonomy*, where decision-making and action is fully within the system's software (and so assessment of why decisions are made becomes crucial); *adaptive systems*, where decision-making and action are driven by (often continuous) interactions with the environment; and *automated systems*, where decision-making and action are pre-scripted, etc. Which form of decision-making is utilised will also have a strong impact on the effectiveness of any of the verification techniques which can be applied to.

V. WHY IS THIS IMPORTANT?

As the range of systems that are expected to act on their own expands, so the need for verification of these autonomous systems becomes more important. When there is a 'human in the loop', i.e. a human providing oversight and control of a system, the key decisions about the system can be delegated to that human, leaving the system analysis to address issues such as reliability. However, once there is a need for the system to make key decisions, much more evidence and confidence in this type of systems will be required. Developing the ability to establish and provide this evidence is then essential not only for Engineers, but for all stake-holders such as Regulators, the Public, and Governments, and so for this Technical Committee.

If the abilities of systems and the environments in which they are to work remain constrained, then realistic boundaries for system behaviours can be provided. However, once autonomous systems are deployed in hard to predict or unknown environments and we expect them to make key, and sometimes (safety, mission, security) critical decisions, then a much stronger analysis is required. In addition, what requirements might be assessed depend crucially on what is known of the system and its environment. Traditionally, it has been assumed that we can assess (before deployment) all potential issues/concerns and mitigate against these, which might be the case in highly controlled, closed environments. However, with autonomous systems increasingly being used in open, uncontrolled environments and with internal, software behaviour able to change in various ways, our ability to predict "everything that might go wrong" is severely limited. Furthermore, stochastic models of complex, unknown environments can never be complete and may have hard-to-predict errors. Therefore, this Technical Committee is concerned with the development of tools and techniques to verify autonomous systems even in such unconstrained and unstructured environments.

VI. TECHNICAL COMMITTEE ORGANISATION

Leadership of the Technical Committee is provided by five people, as follows.

A. Technical Committee Co-Chairs:

- Dejanira Araiza-Illan
- Michael Fisher
- Signe Redfield

B. Technical Committee Junior Co-Chairs:

- Kevin Leahy
- Joanna Isabelle Olszewska

VII. TECHNICAL COMMITTEE ACTIVITIES

The Technical Committee engages in a range of activities:

- Monthly hour-long webinars, each comprising 2×20 minute talks, with questions (and answers) and an approximate average number of attendees of 55;
- Annual meetings to discuss existing activities, collate feedback, and define new activities;
- Sponsorship of workshops, such as at IROS or ICRA; and
- Input into IEEE standards, such as IEEE 2817 [5] and IEEE P7009 [4].

As an example, the P7009 Standards Working Group, which studies the “Fail-safe Design of Autonomous Systems”, asked this Technical Committee for input concerning autonomous systems verification. Thus, a subgroup among TC-VAS was created, met regularly, and subsequently provided the P7009 team with a summary document to be included in their standard.

VIII. TECHNICAL COMMITTEE FUTURE PLANS

The Technical Committee also has plans for a range of future activities:

- A **Roadmap** highlighting research challenges and developments required over the medium to long term;
- A range of **Educational Resources** on the “verification of autonomous systems” topic;
- A **Catalog and Repository of Tools** available for some aspect of the verification of autonomous systems;
- Continued sponsorship of workshops at CASE, IROS, or ICRA, but also a stand-alone event dedicated to the verification of autonomous systems.

IX. CLOSING REMARKS

Individuals can become involved with this Technical Committee by visiting our home page at <https://www.ieee-ras.org/verification-of-autonomous-systems> and selecting the **Join Us** menu option. This will bring up a form that adds new members to our mailing list.

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